

U.S. Department of Commerce, Patent and Trademark Office		Atty Docket No.	Serial No.
		CIS0071US	09/821,948
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		Applicant(s)	
		Douglas E. Duschatko, Andrew J. Thurston	
MAR 04 2003		Filing Date	Group
		March 30, 2001	2184

## U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						RECEIVED
	AF						MAR 06 2003
	AG						Technology Center 2100
	AH						
	AI						
	AJ						
	AK						

## Foreign Patent Documents

		Document	Date	Country	Class	Subclass	Translation
	AL						
	AM						
	AN						
	AO						
	AP						

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

DT	AQ	Christian Schuler, "Code Generation Tools For Hardware Implementation Of FEC Circuits," GMD FOKUS, Kaiserin-Augusta-Allee 31, 10589 Berlin, Germany.
DT	AR	Shu Lin and Daniel J. Costello, Jr., "Error Control Coding <i>Fundamentals and Applications</i> ," Prentice-Hall, Inc. Englewood Cliffs, New Jersey, Chapter 6, pp. 141-183.
	AS	

Examiner Dudson Date Considered8/20/04

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.